N THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Daniel G. Borkowski et al.

Title:

CYCLE-EFFICIENT TDM TIMESLOT REMAPPING FOR NETWORK PROCESSING

**ENGINES** 

Docket No.:

Intel-007PUS

Filed: Examiner:

November 26, 2003

Hailu, Kimbrom T.

Serial No.: 10/723,804

Due Date: N/A

Group Art Unit: 2616

MS Issue Fee

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

X Return postcard and transmittal document.

X An Amendment Under 37 C.F.R. §1.312 (13 Pages).

Customer Number: 45780

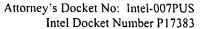
Anthony T. Moose

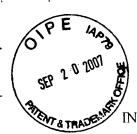
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<u>CERTIFICATE UNDER 37 CFR 1.8:</u> The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this <u>18th</u> day of September, 2007.

hellie Bailey

(GENERAL)





## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Borkowski et al. Art Unit: 2616

Serial No.: 10/723,804 Examiner: Hailu, Kibrom T. Filed: November 26, 2003 Assignee: Intel Corporation

Title : CYCLE-EFFICIENT TDM TIMESLOT REMAPPING FOR NETWORK

PROCESSING ENGINES

## MAIL STOP ISSUE FEE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## 37 C.F.R. § 1.312 AMENDMENT

Please amend the above-identified application as follows: